

Docket No.: 22173-70304
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Franciscus P. Budzelaar

Application No.: 10/598,642

Confirmation No.: 8420

Filed: September 7, 2006

Art Unit: 4163

For: ACTIVE MATRIX DISPLAY DEVICE

Examiner: K. Sharifi-Tafreshi

COMMUNICATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

November 24, 2009

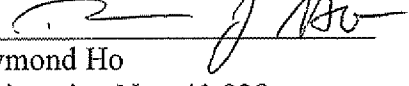
Dear Madam:

In response to the telephonic conversation today, November 24, 2009, with Examiner Tafreshi, Applicant has agreed with the Examiner to rewrite claim 1 to incorporate claim 5 to put this application in condition for allowance.

Enclosed is a proposed Amendment for Examiner's consideration thereof.

Applicant believes no fee is due with this response. However, if a fee is due, please charge our Deposit Account No. 50-3537, under Order No. 22173-70304 from which the undersigned is authorized to draw.

Respectfully submitted,

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PROPOSED AMENDMENTS TO THE CLAIMS

Listing of claims:

1. (Currently Amended) Active matrix display device (6) comprising a display panel (2) with a matrix of display pixels (3), and row and column electrodes (11,12) coupled to said display pixels (3), each of said display pixels (3) having a current mirror circuit adapted to receive a programming current (I_{prog}) via said column electrodes (11) and to reproduce said programming current (I_{prog}) for driving an emissive element (14), wherein said display device (6) is further arranged to execute a calibration phase wherein a calibration voltage (V_{cal}) is applied at each column electrode (11) before said programming current (I_{prog}) is applied and said calibration voltage is substantially maintained at said column electrode (11) for each of said display pixels (3) until said programming current (I_{prog}) is applied, and wherein each of said display pixels (3) further comprises calibration circuitry having a capacitor (C_{cal}) and a transistor (T_{cal}) whose current carrying electrodes are connected between said column electrode (11) and a first plate of said capacitor (C_{cal}), and is arranged to charge said capacitor (C_{cal}) prior to said calibration phase and to discharge during said calibration phase via said transistor (T_{cal}) such that the gate of said transistor (T_{cal}) carries a voltage substantially equal to the sum of said calibration voltage (V_{cal}) and a threshold voltage (V_t) of said transistor (T_{cal}).

2. (Original) Active matrix display device (6) according to claim 1, wherein said display device (6) is arranged for simultaneous execution of said calibration phase for more than one row (4) of said display pixels (3).

3. (Original) Active matrix display device (6) according to claim 1, wherein each of said column electrodes (11) is coupled to at least one switch (S_{cal}) to apply said calibration voltage (V_{cal}).

4. (Original) Active matrix display device (6) according to claim 3, wherein said switch (S_{cal}) connects said column electrodes (11) to ground.

5. (Canceled).

6. (Currently Amended) Active matrix display device (6) according to claim 5 1, wherein said calibration circuitry comprises one or more switches (S5, S6) to control said charging and discharging of said capacitor (C_{cal}) and wherein said display device (6) comprises a display controller (7) for controlling said switches (S5,S6).

7. (Currently Amended) Active matrix display device (6) according to claim 5 1, wherein a second plate of said capacitor (C_{cal}) is connected either to ground or to a substantially constant voltage supply.

8. (Original) Active matrix display device (6) according to claim 1, wherein said display device (6) comprises common calibration circuitry to execute said calibration phase for several display pixels (3) along said column electrode (11).

9. (Original) A product (1) comprising the active matrix display device (6) as claimed in claim 1; and signal processing circuitry (SP) for supplying a signal to the active matrix display device (6).

10. (Canceled).

11. (Canceled).